

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Paul Stephan Bedrosian

Case:

7

Serial No.:

To Be Assigned

Filing Date:

June 14, 1999

Title:

Method and Apparatus for Synchronization of

High-Bit-Rate Digital Subscriber Line Signals

Group:

To Be Assigned

Examiner:

To Be Assigned

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed document is enclosed.

- 1. T1E1.4/99-006R1 HDSL2 Draft Standard, pp. 1-61, March 1999.
- 2. ANSIT1.101-1994, Synchronization Interface Standard, pp. i-iv and 1-57, 1994.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

Joseph B. Ryan Reg. No. 37,922

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